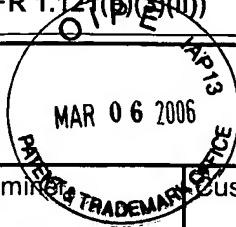


**SUBMISSION OF PROPOSED DRAWING AMENDMENT FOR APPROVAL BY
EXAMINER (37 CFR 1.121(a) or 37 CFR 1.121(b)(2)(ii))**

Docket No.
L8462.03118

In Re Application Of: **Kiyohito MUKAI, et al.**



Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.
10/715,119	November 18, 2003	N. Doan	52989	2825	2755

Invention: **SEMICONDUCTOR DEVICE LAYOUT INSPECTION METHOD**

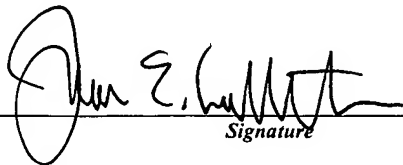
Address to:
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Attached please find:

(check applicable items)

- ☒ a sketch in permanent ink
- ☒ a copy of the original drawing(s) with red ink

showing proposed changes to the drawing(s) in this application for which the approval of the examiner is requested.


Signature

James E. Ledbetter, Esq.
Registration No. 28,732
Stevens, Davis, Miller & Mosher, LLP
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Tel: 202-785-0100
Fax: 202-408-5200

Dated: **March 6, 2006**

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on

(Date)

Signature of Person Mailing Correspondence

Typed or Printed Name of Person Mailing Correspondence